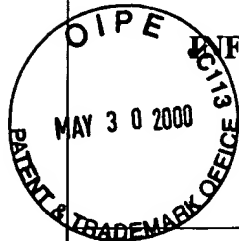


INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Docket: 2791-52913		App: 09/507,261	
				Applicant: Safranek, et al.			
				Filed: February 18, 2000		Art Unit: 2776 2187 JUN 01 2000	
U.S. PATENT DOCUMENTS							
Group 2700							
Init.*		Number	Date	Name	Class	Sub	Filed
R1E		5,802,578	9/1/1998	Lovett	711	147	
OTHER DOCUMENTS							
R1E			James Archibald, et al., ACM Transactions on Computer Systems, Vol. 4 No. 4, November 1986, "Cache Coherence Protocols: Evaluation Using a Multiprocessor Simulation Model"				
R1E			MESI Protocol, at URL: http://www.disi.unige.it/person/DelzannoG/CacheProtocol/mesi.html				
R1E			Ivan Tving, Master Thesis DTH ID-E 579., February 2, 1994, Multiprocessor Interconnection Using SCI				
EXAMINER: <i>Rha S. Eha</i>				DATE 12/14/01			
*Examiner: Initial if considered, whether or not in conformance with MPEP 60; draw line through cite if not in conformance and not considered. Send copy.							


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